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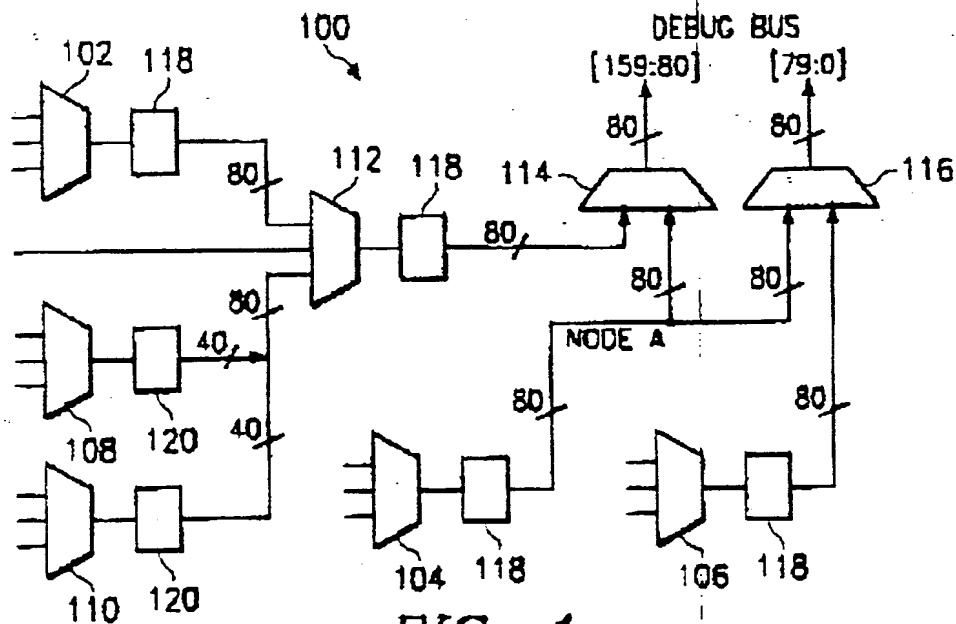


FIG. 1

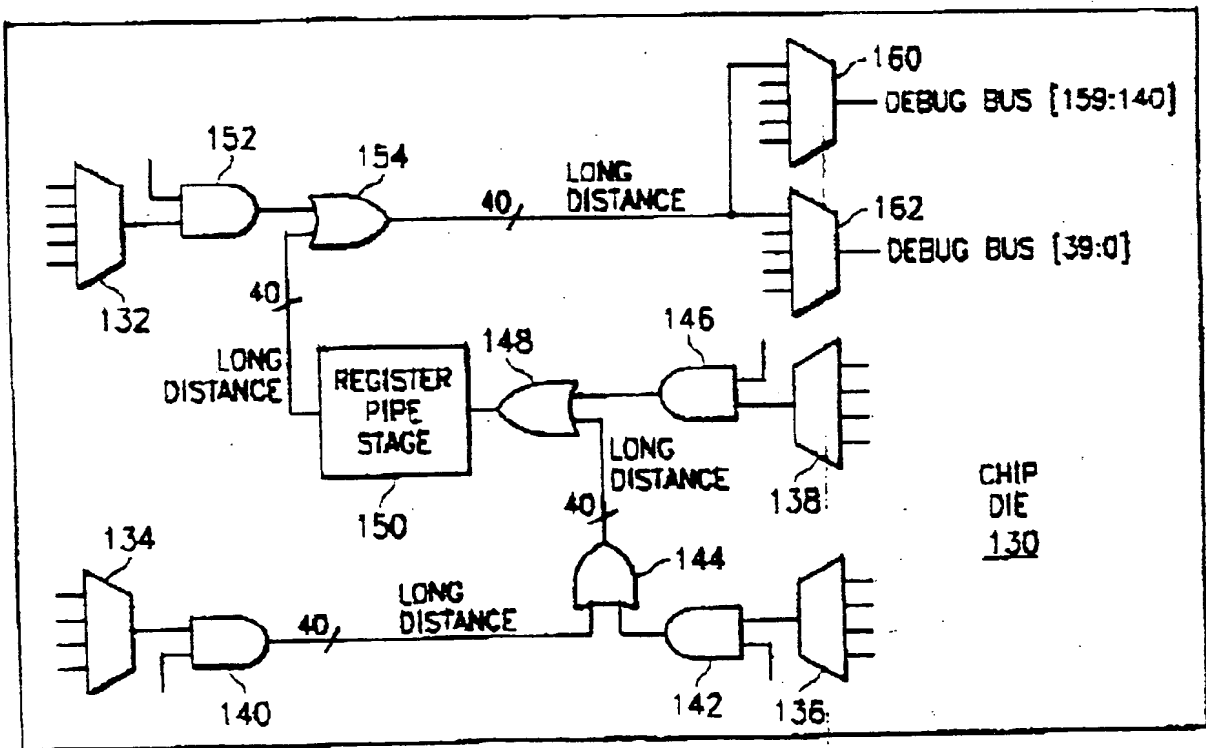


FIG. 2

A SYSTEM AND METHOD FOR MULTIPLE CYCLE
CAPTURE OF CHIP STATE

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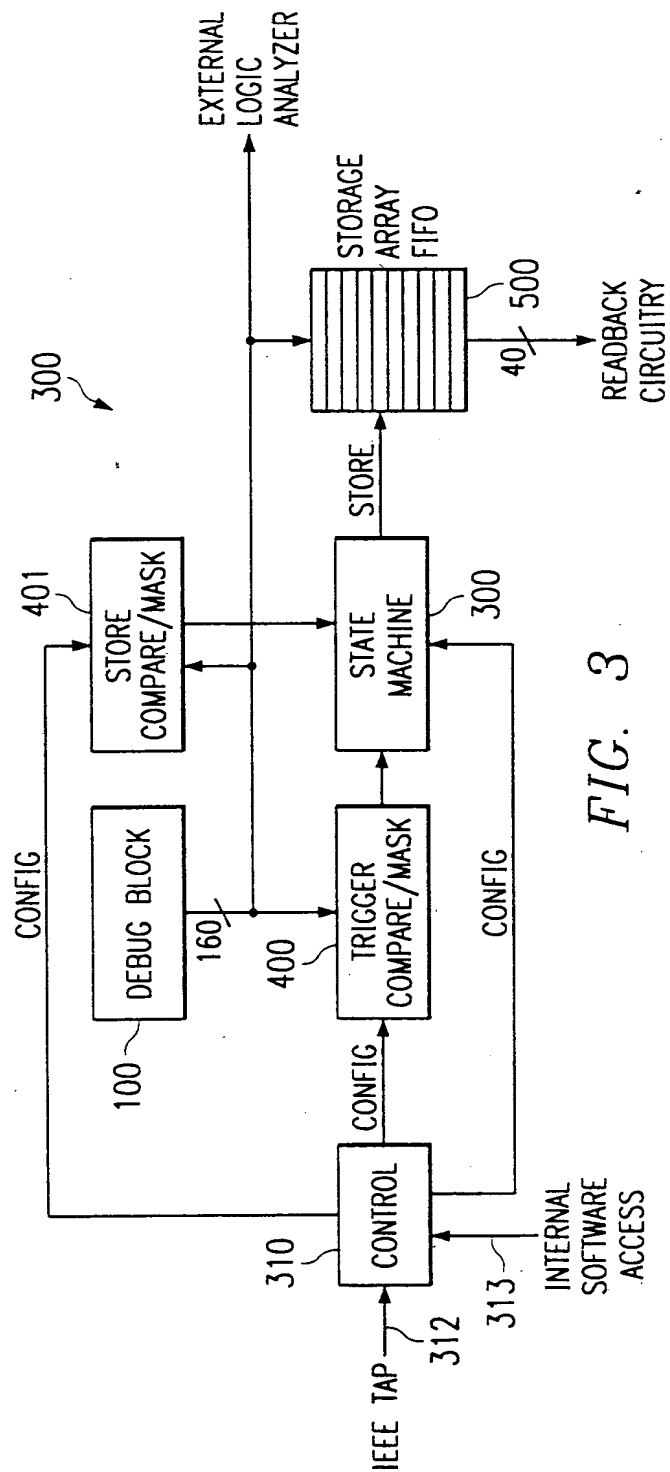
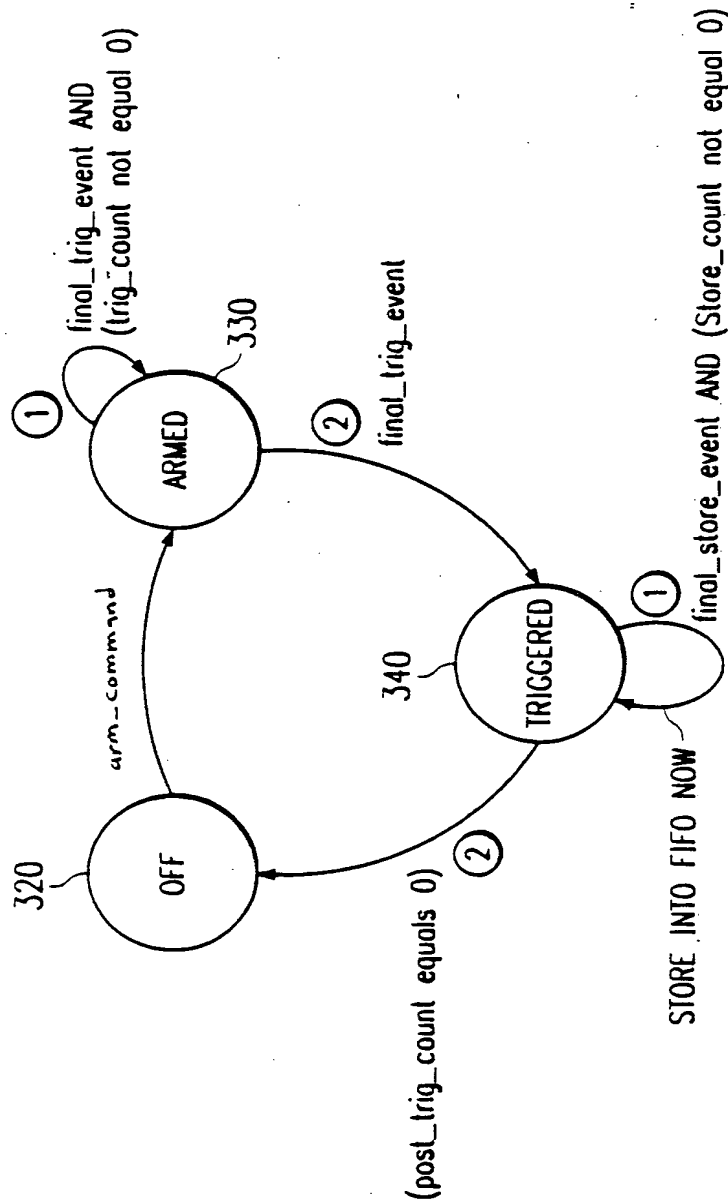


FIG. 3

FIG. 4



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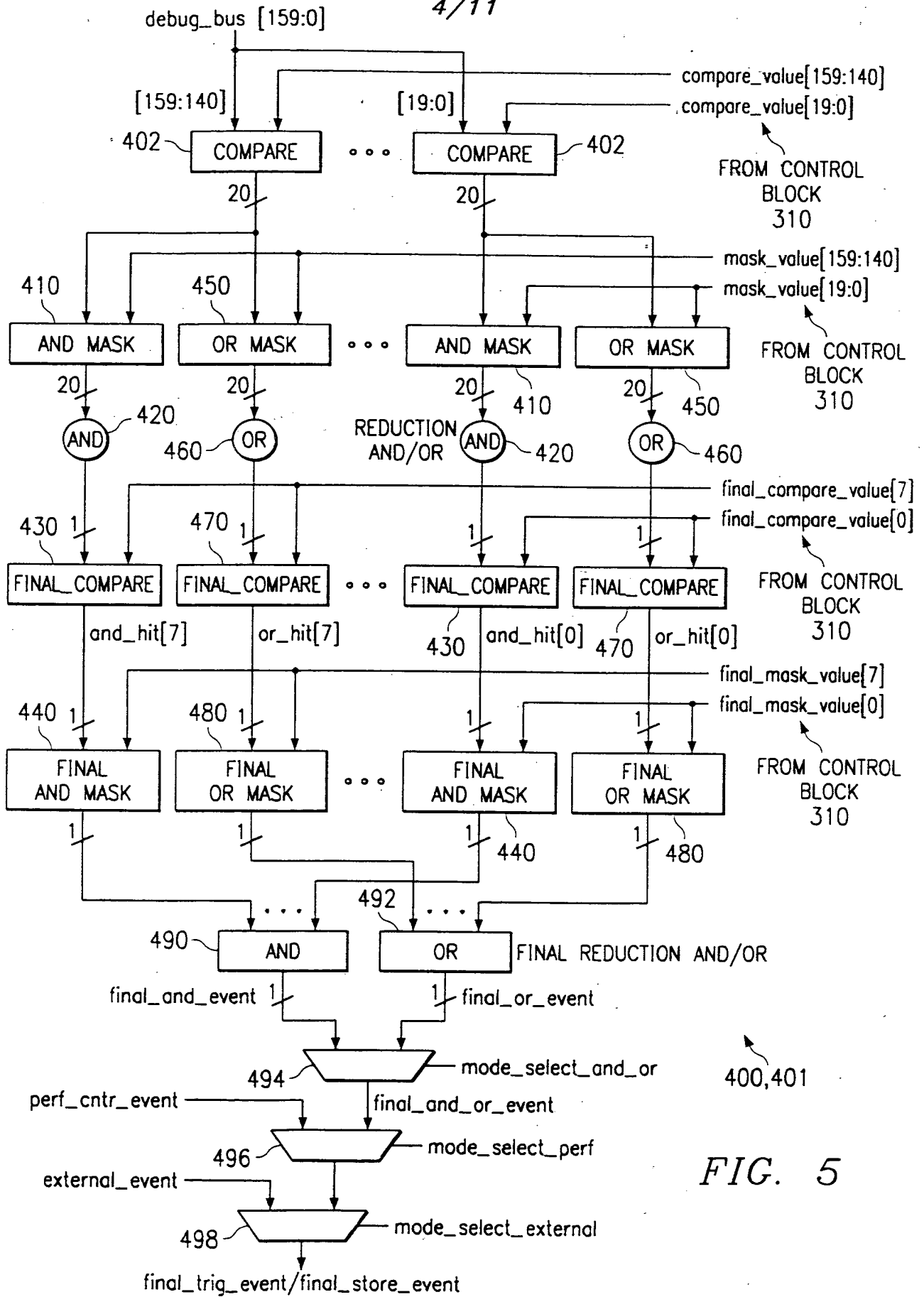


FIG. 5

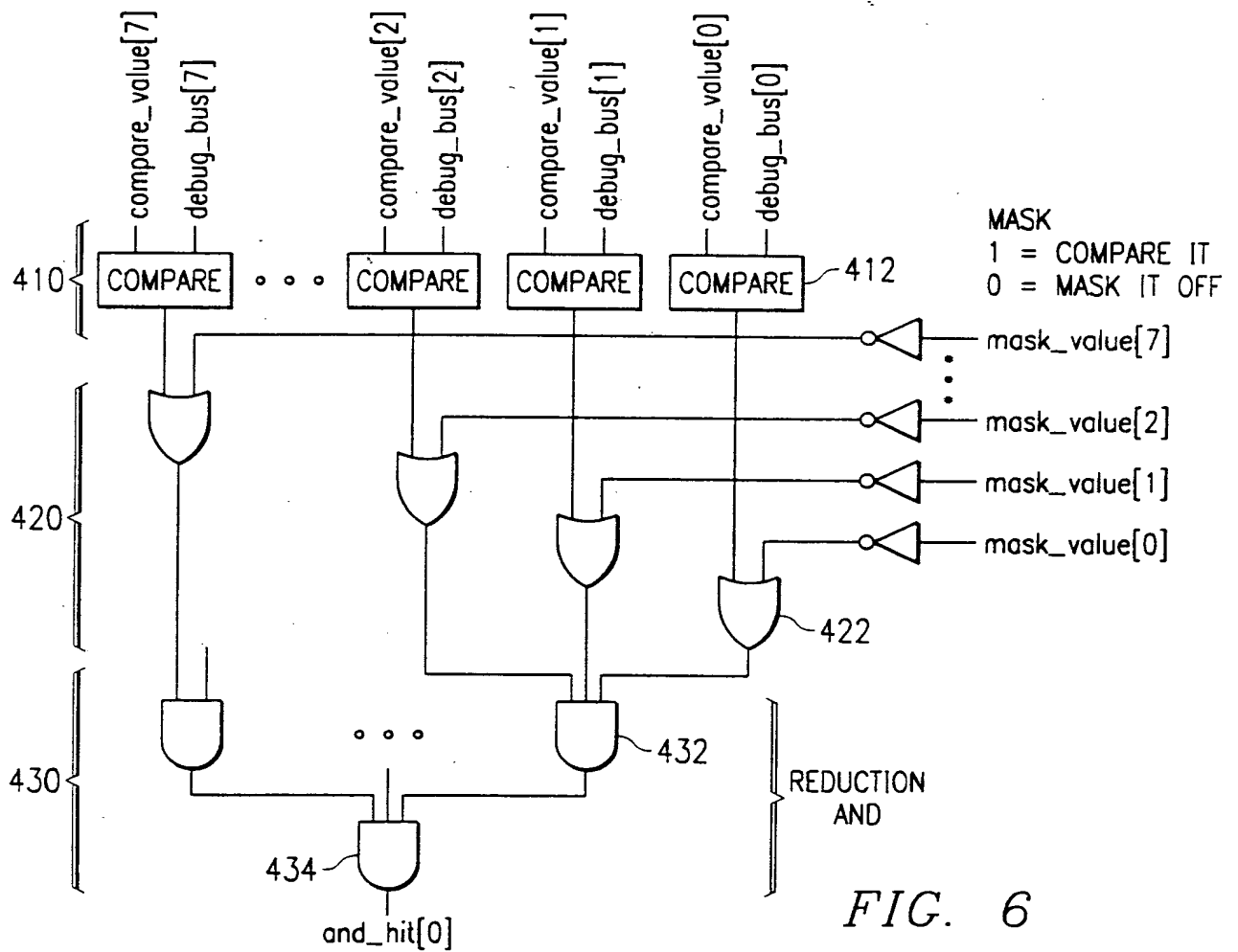
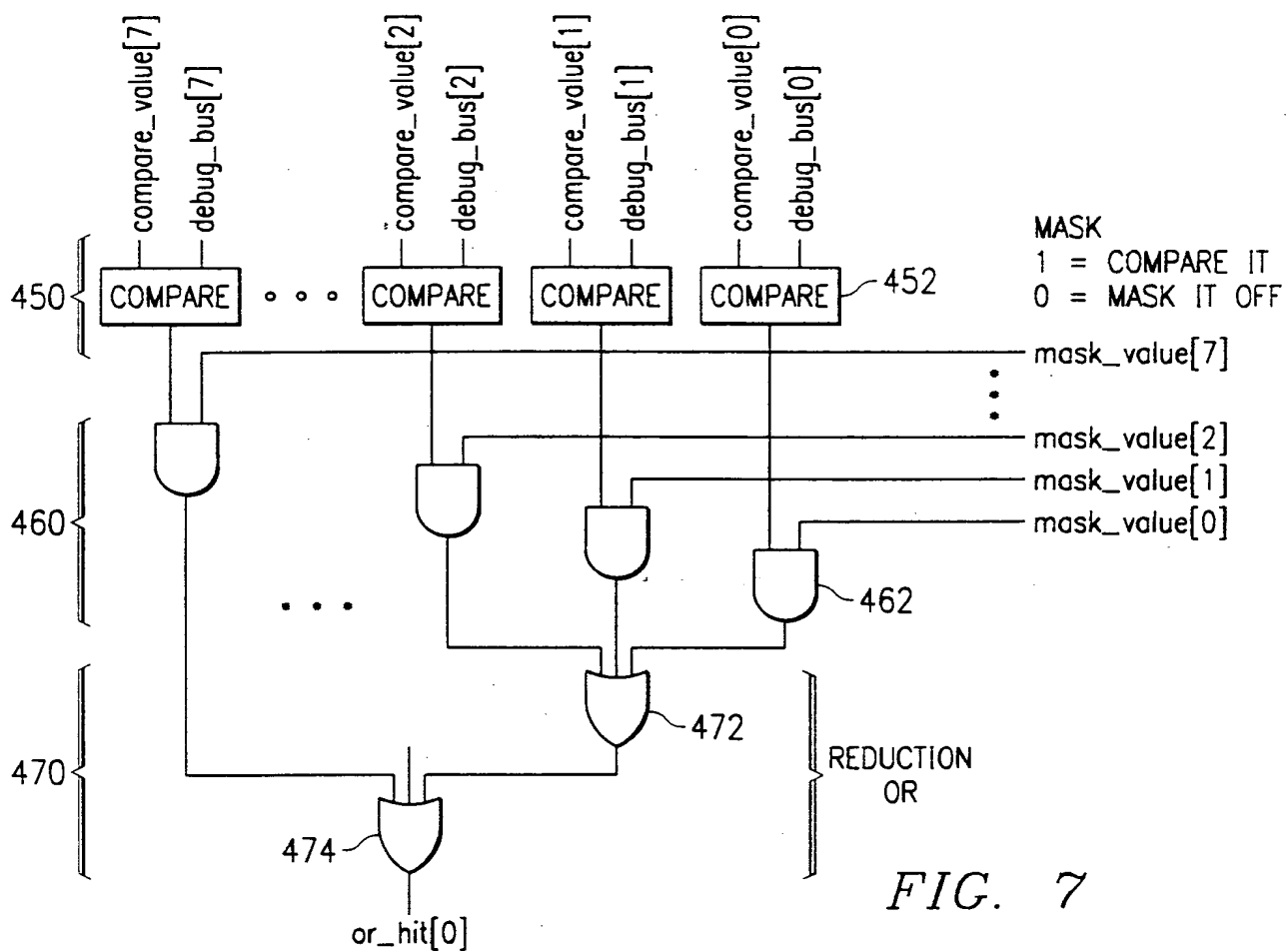


FIG. 6



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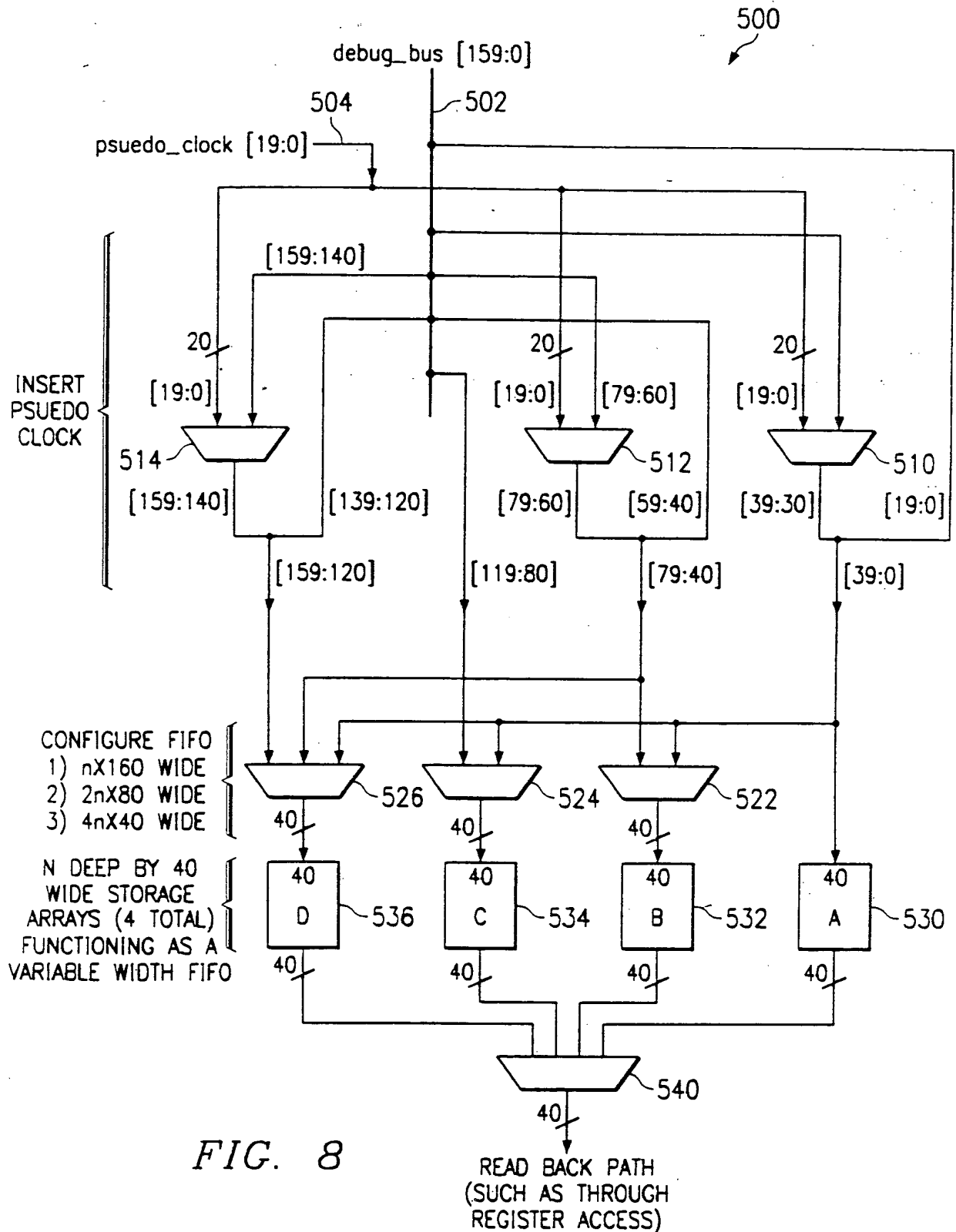


FIG. 8

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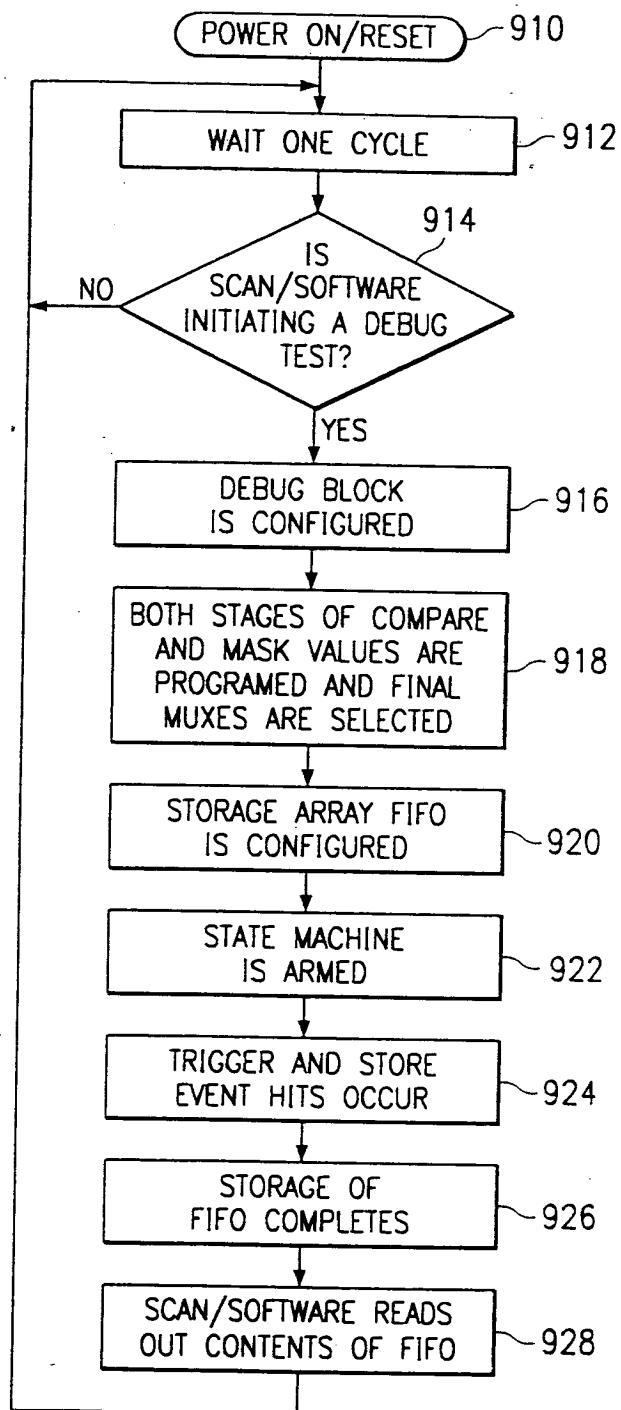


FIG. 9

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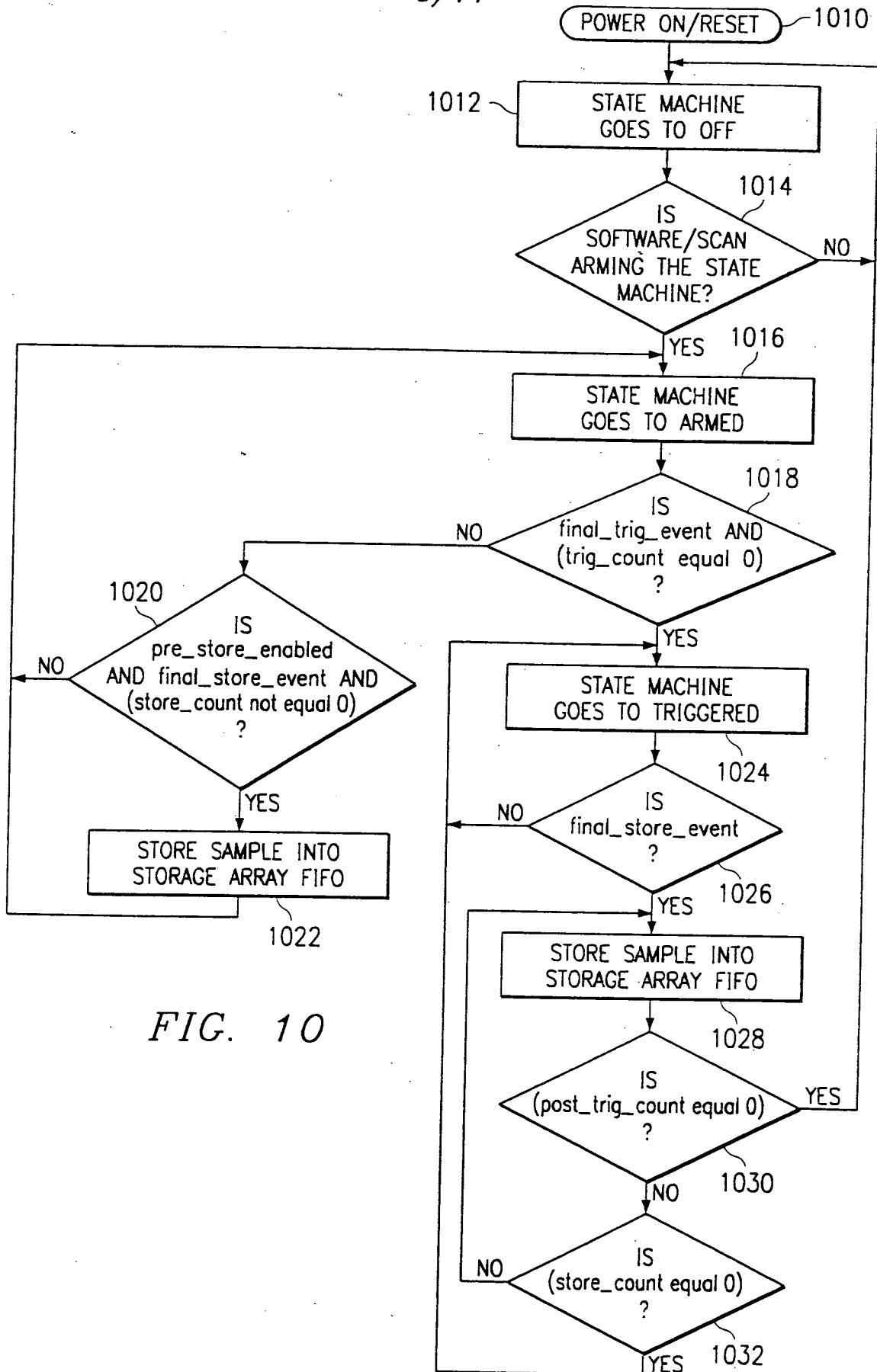


FIG. 10

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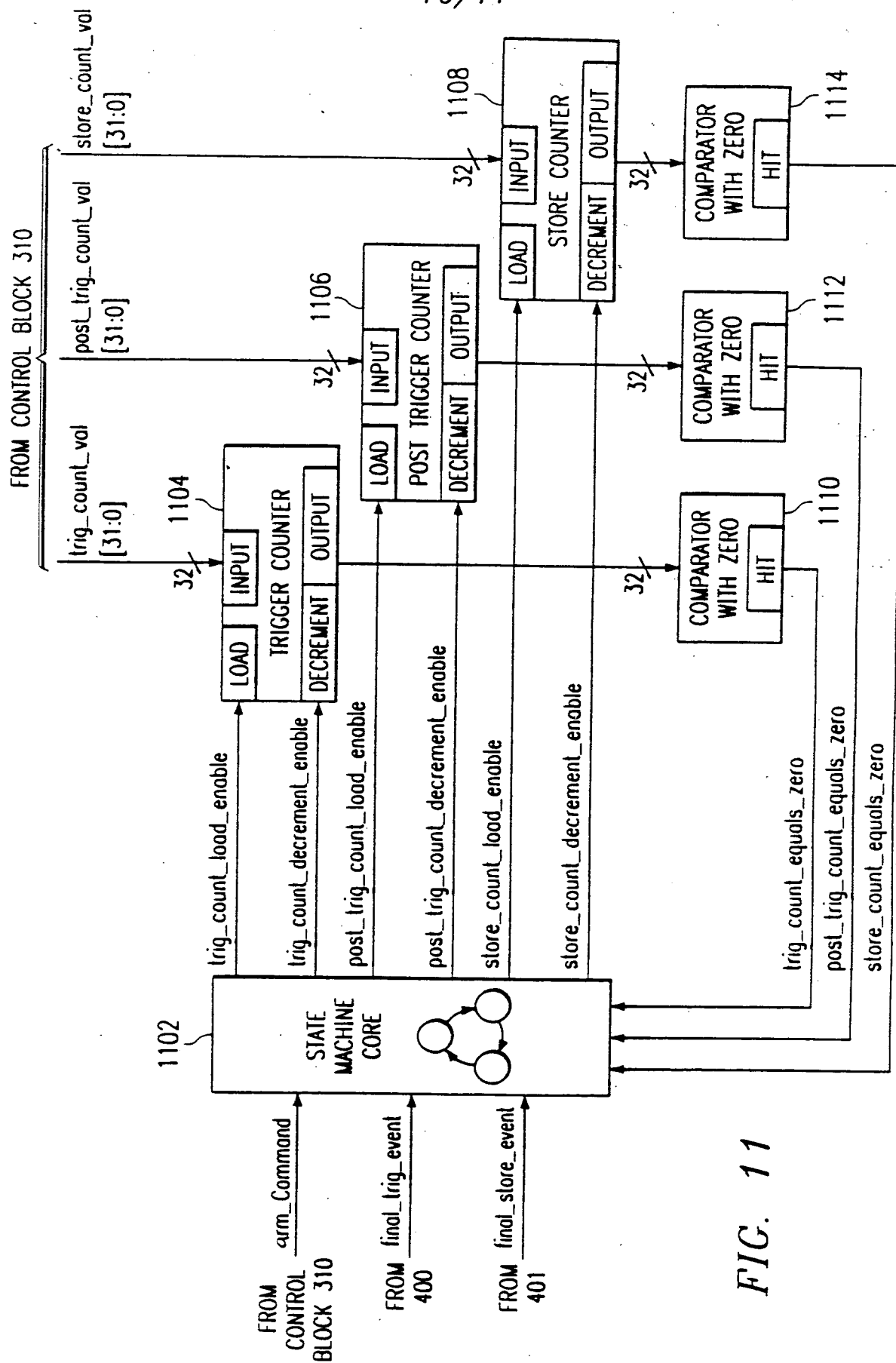


FIG. 11

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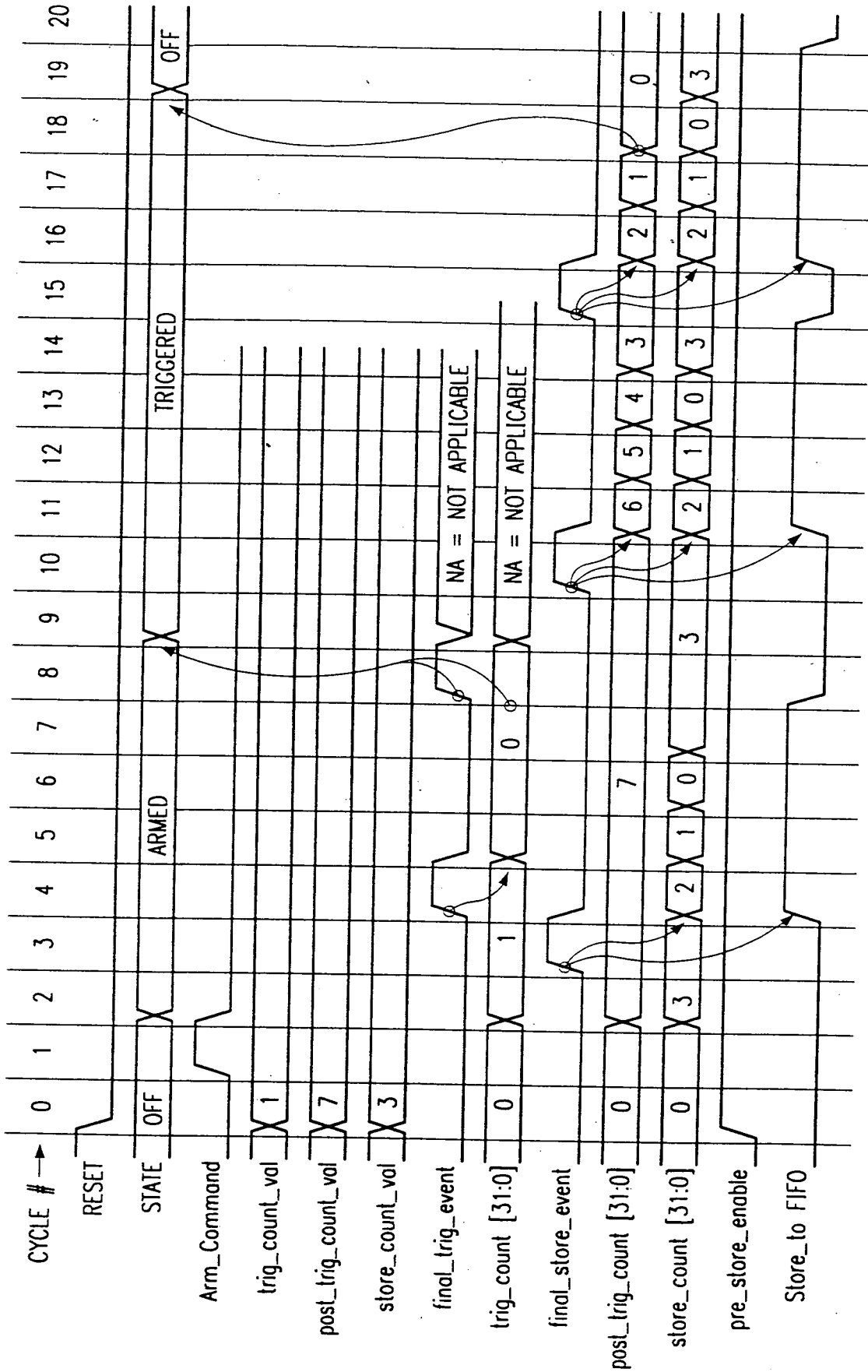


FIG. 12